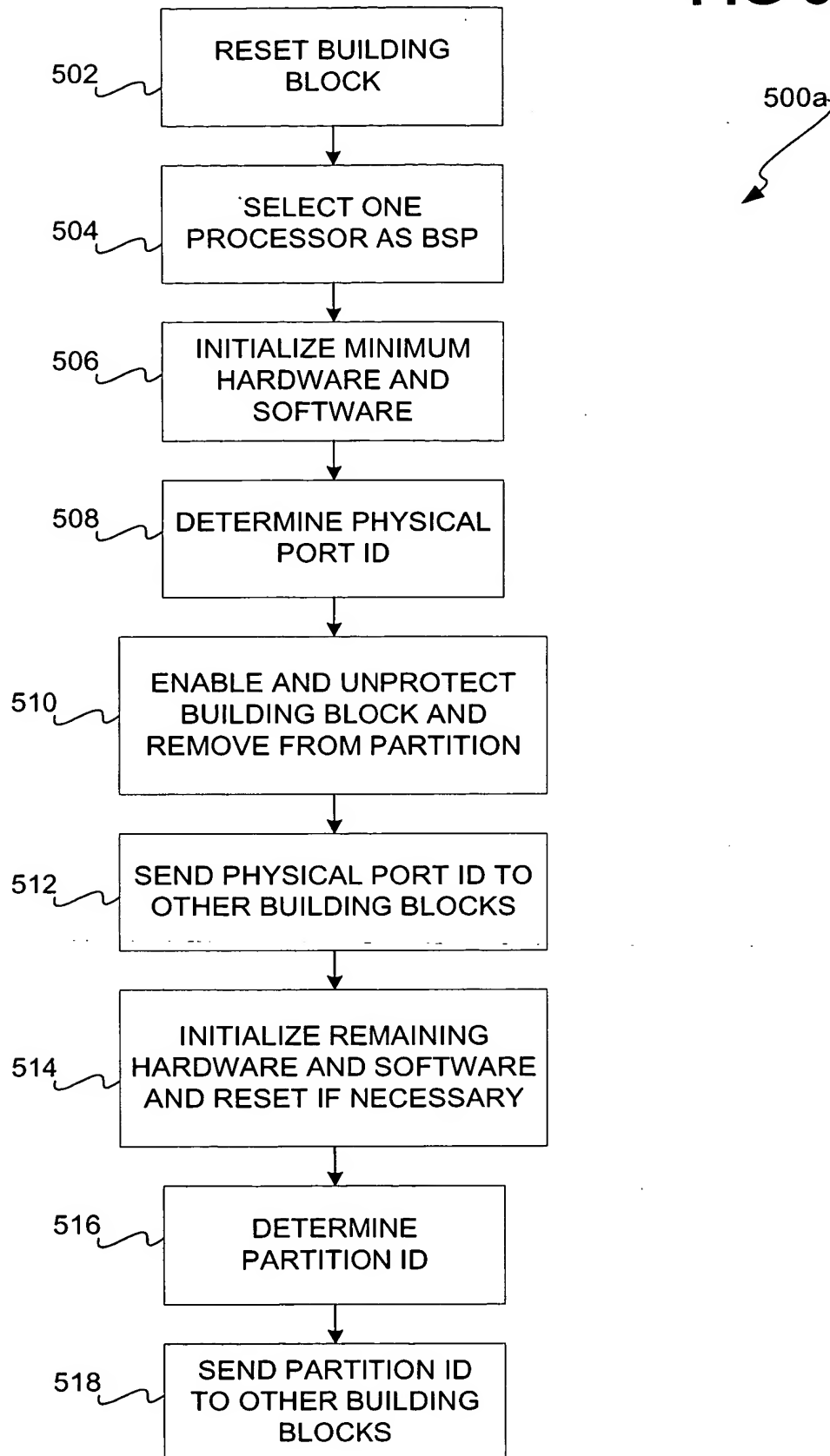
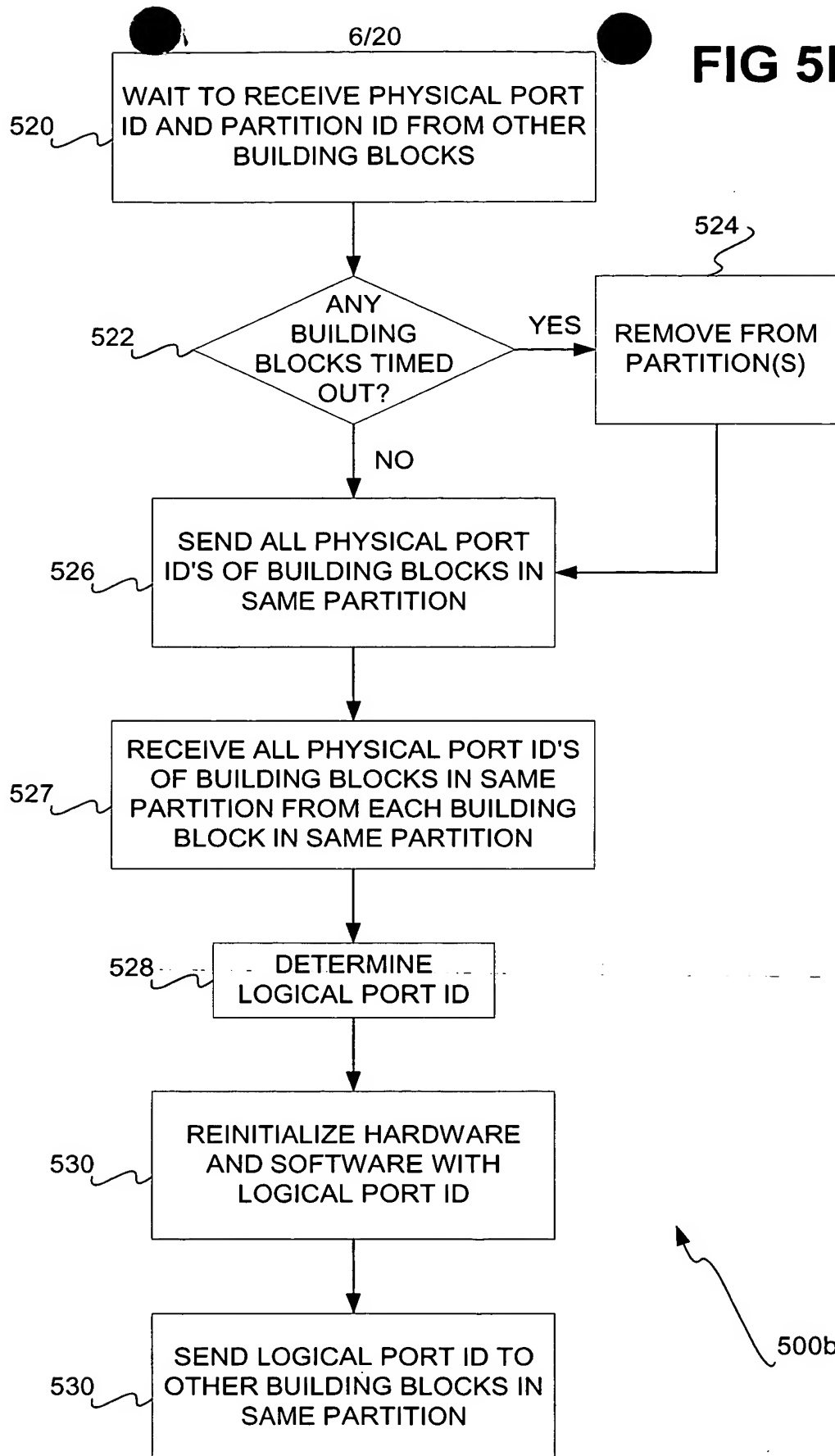


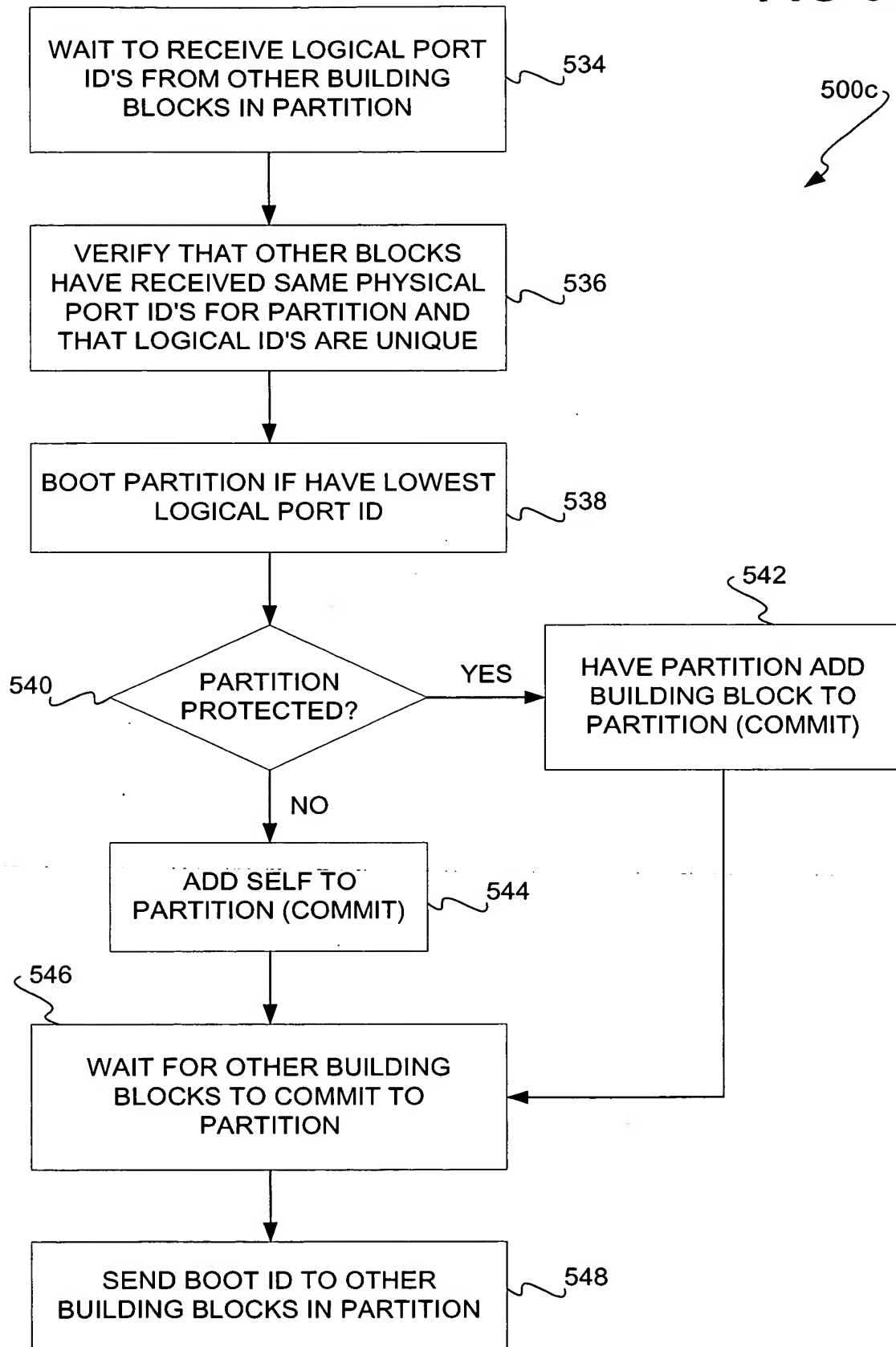
| BUILDING BLOCK ID'S AND INDICATORS |            | 400 |
|------------------------------------|------------|-----|
| PHYSICAL PORT ID                   | <u>402</u> |     |
| LOGICAL PORT ID                    | <u>404</u> |     |
| PARTITION ID                       | <u>406</u> |     |
| PROTECT INDICATOR                  | <u>408</u> |     |
| COMMIT INDICATOR                   | <u>410</u> |     |
| STATUS INDICATOR                   | <u>412</u> |     |

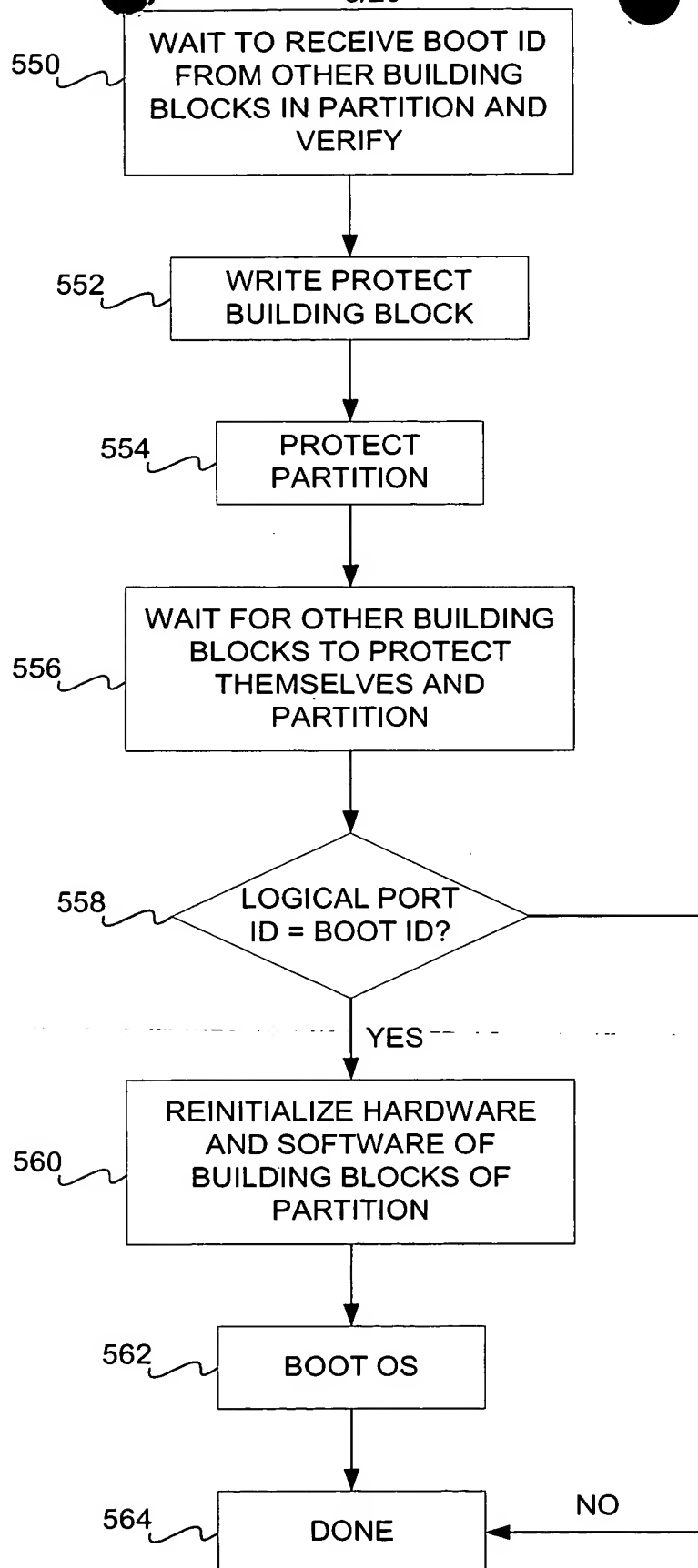
20250709 09:54:07

FIG 5A

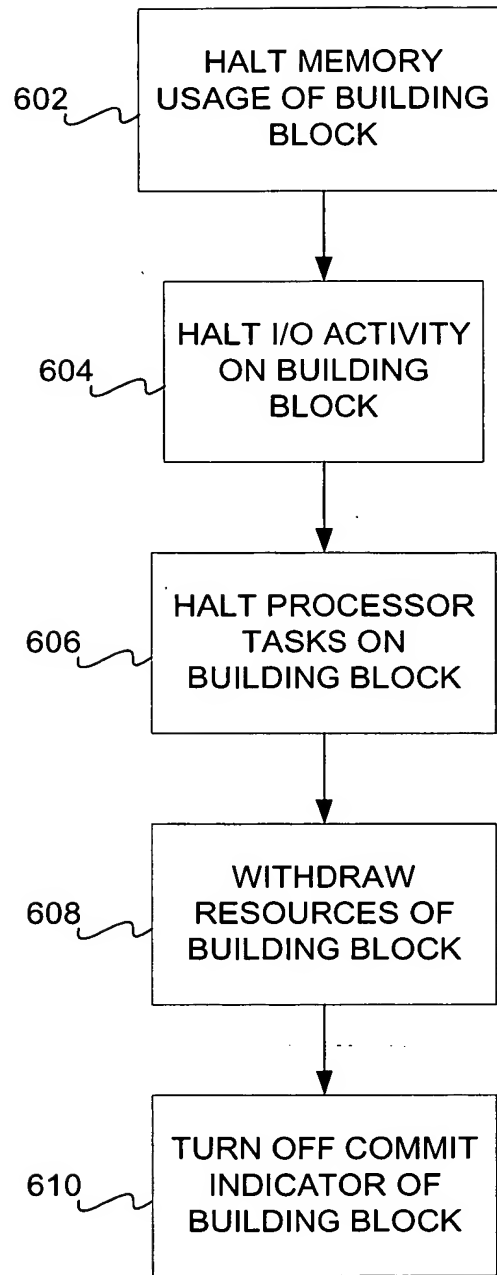












600

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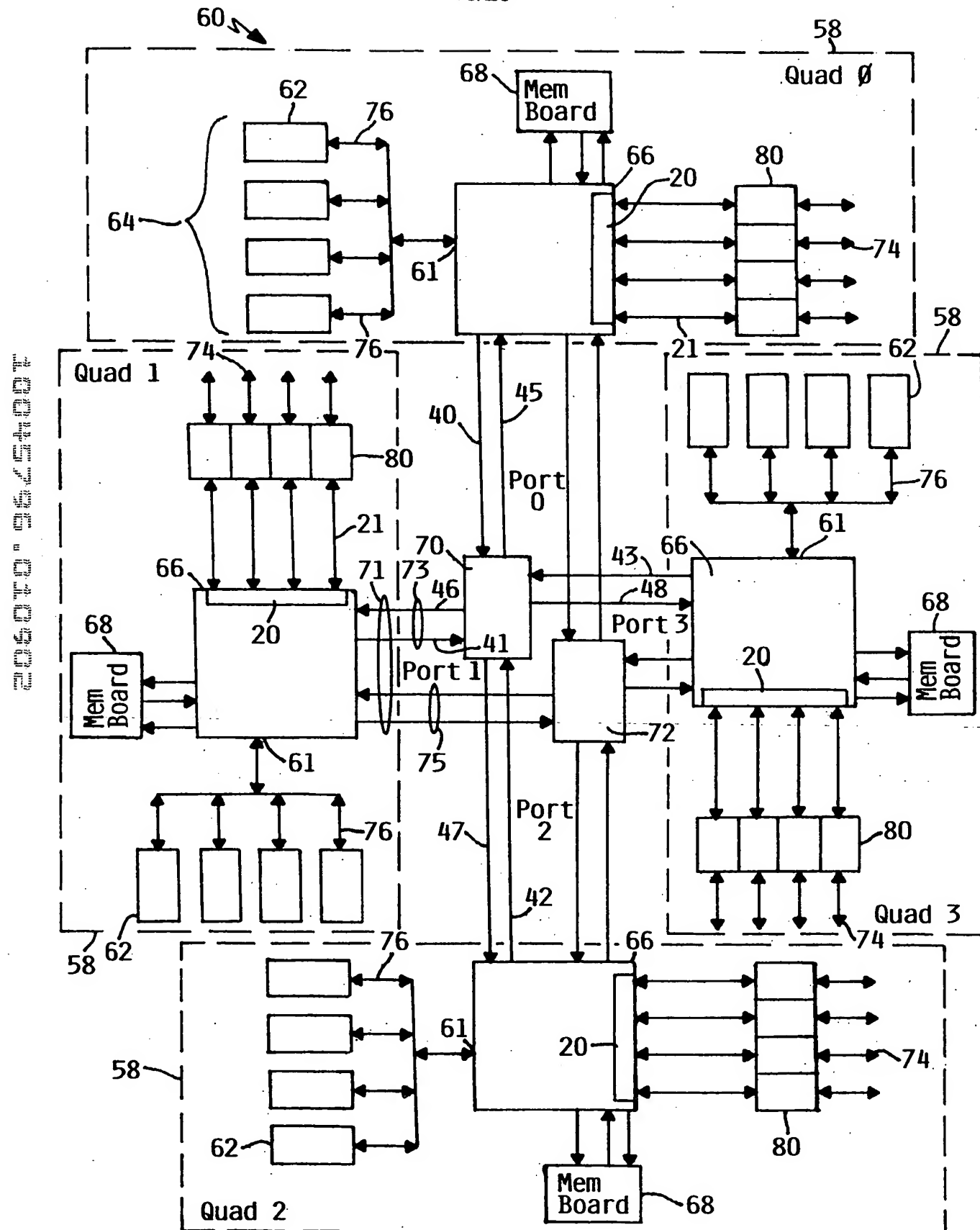


FIG. 7

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32GB-1B

0x7\_FFFF\_FFFF

High Extended  
Memory

4GB

4GB-16MG

4GB-20MG

4GB-32MG

High BIOS (PCI-0)

0x1\_0000\_0000

Processor Specific

Chipset Specific

0x0\_FE00\_0000

PCI-0

PCI-1

PCI-2

PCI-3

MMIO Space  
(512MB per PCI)

2GB

0x0\_8000\_0000

Medium Extended  
Memory

16MB

0x0\_0100\_0000

Low Extended  
Memory

1MB

0x0\_0010\_0000

Segments 8 - F  
(Mapped to PCI or Mem)

512KB

0x0\_0008\_0000

Memory

0x0\_0000\_0000

FIG. 8

2005070" 9625400T

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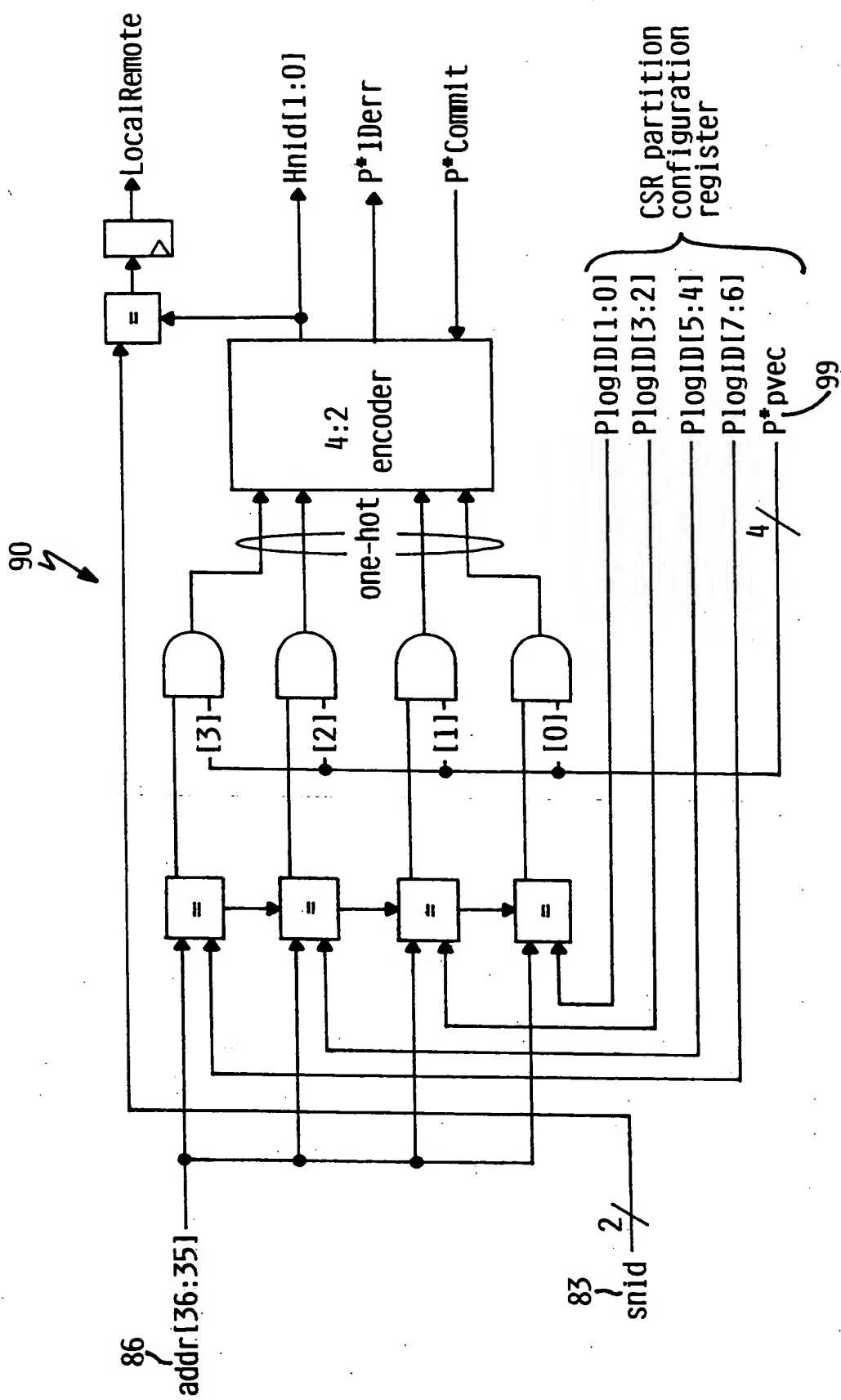


FIG. 9

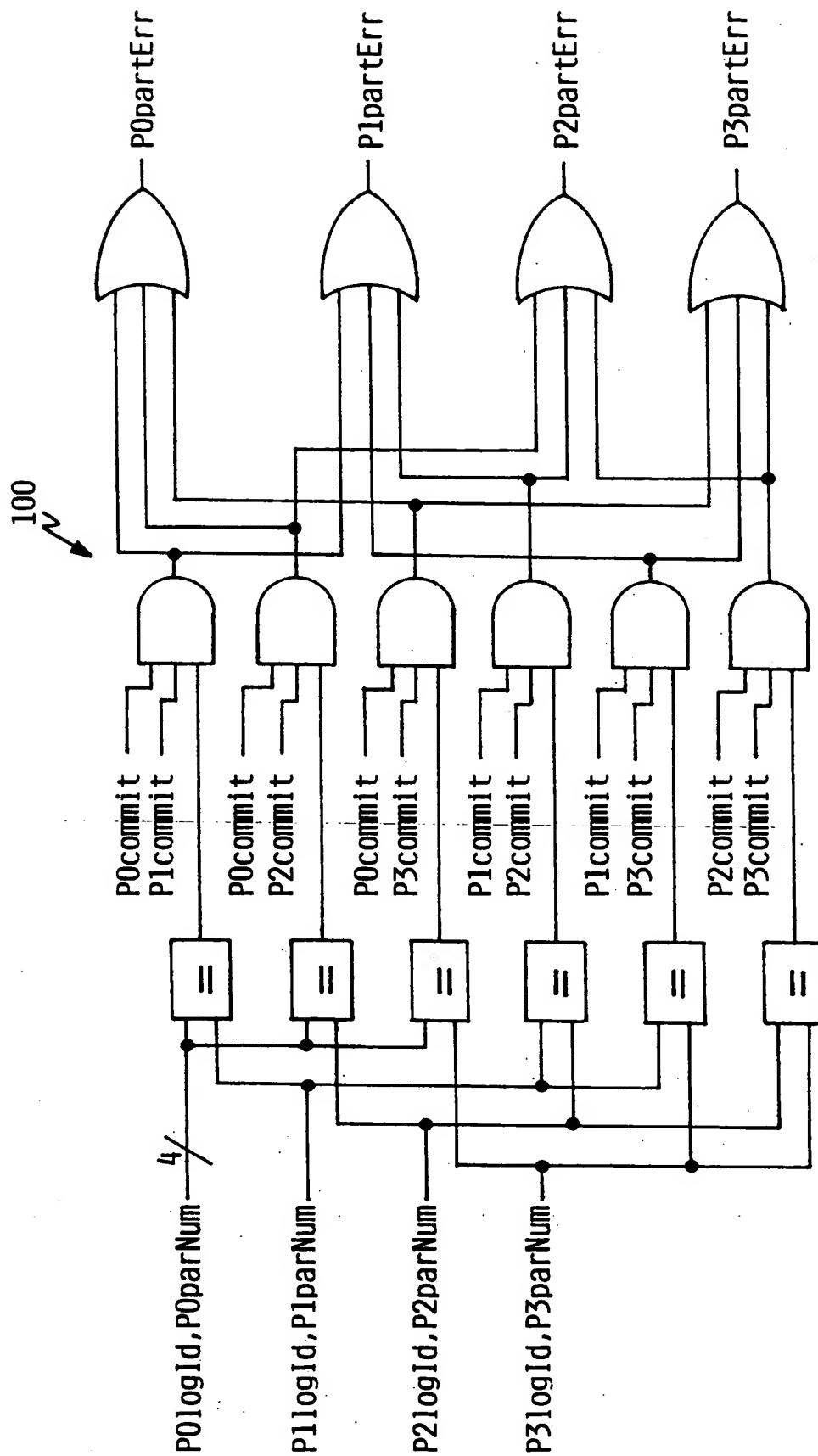


FIG. 10

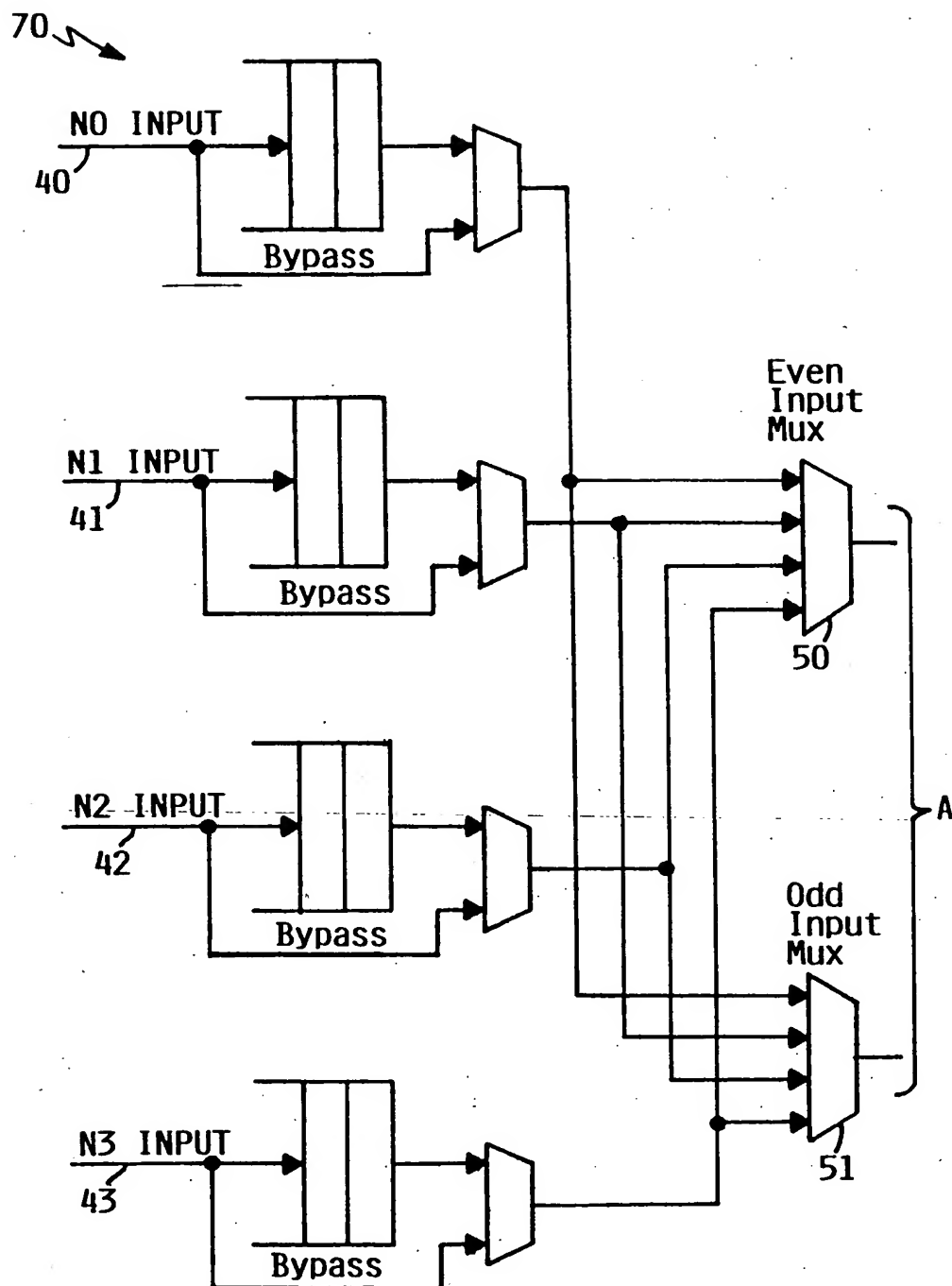


FIG. 11A

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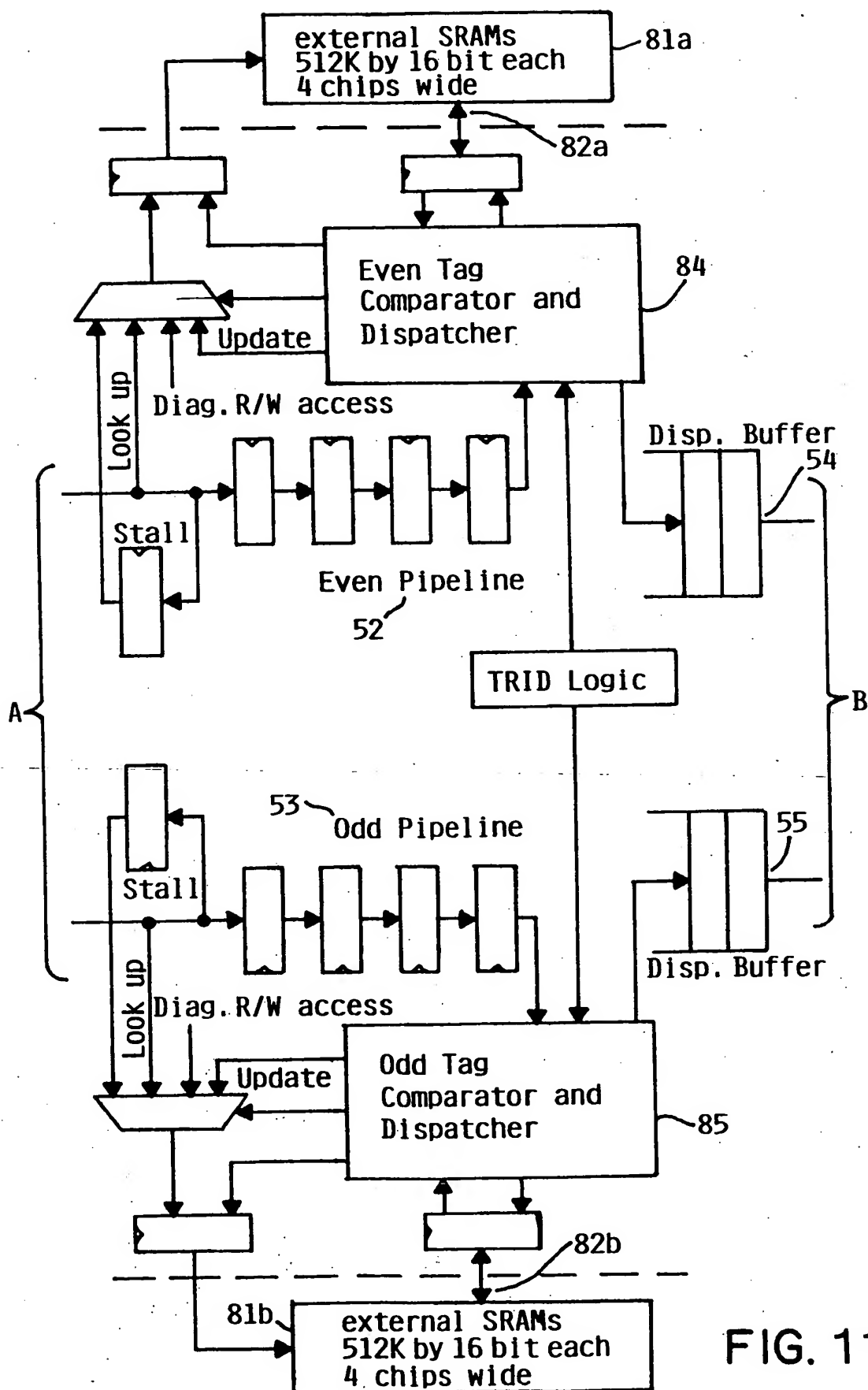


FIG. 11B

16/20

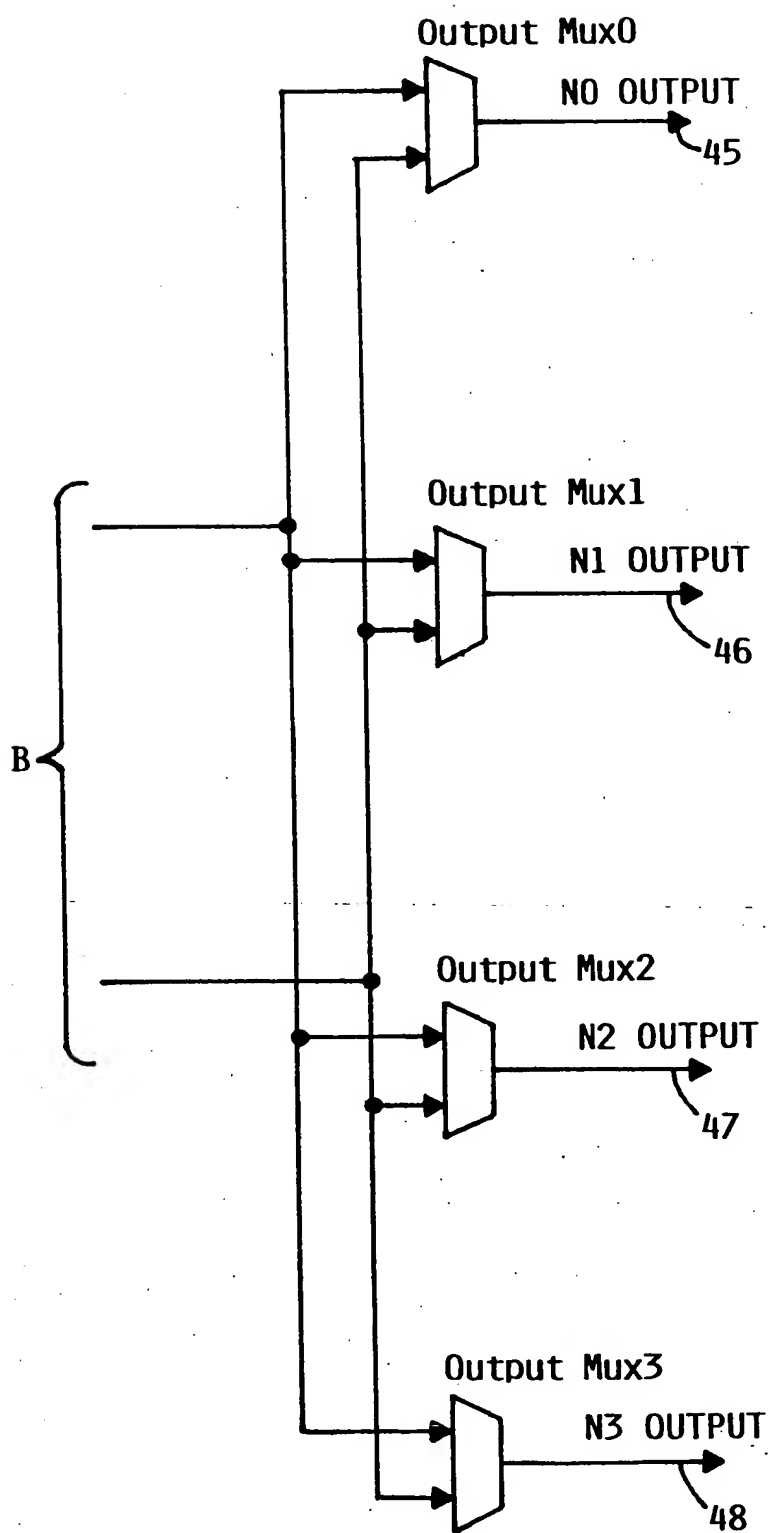


FIG. 11C



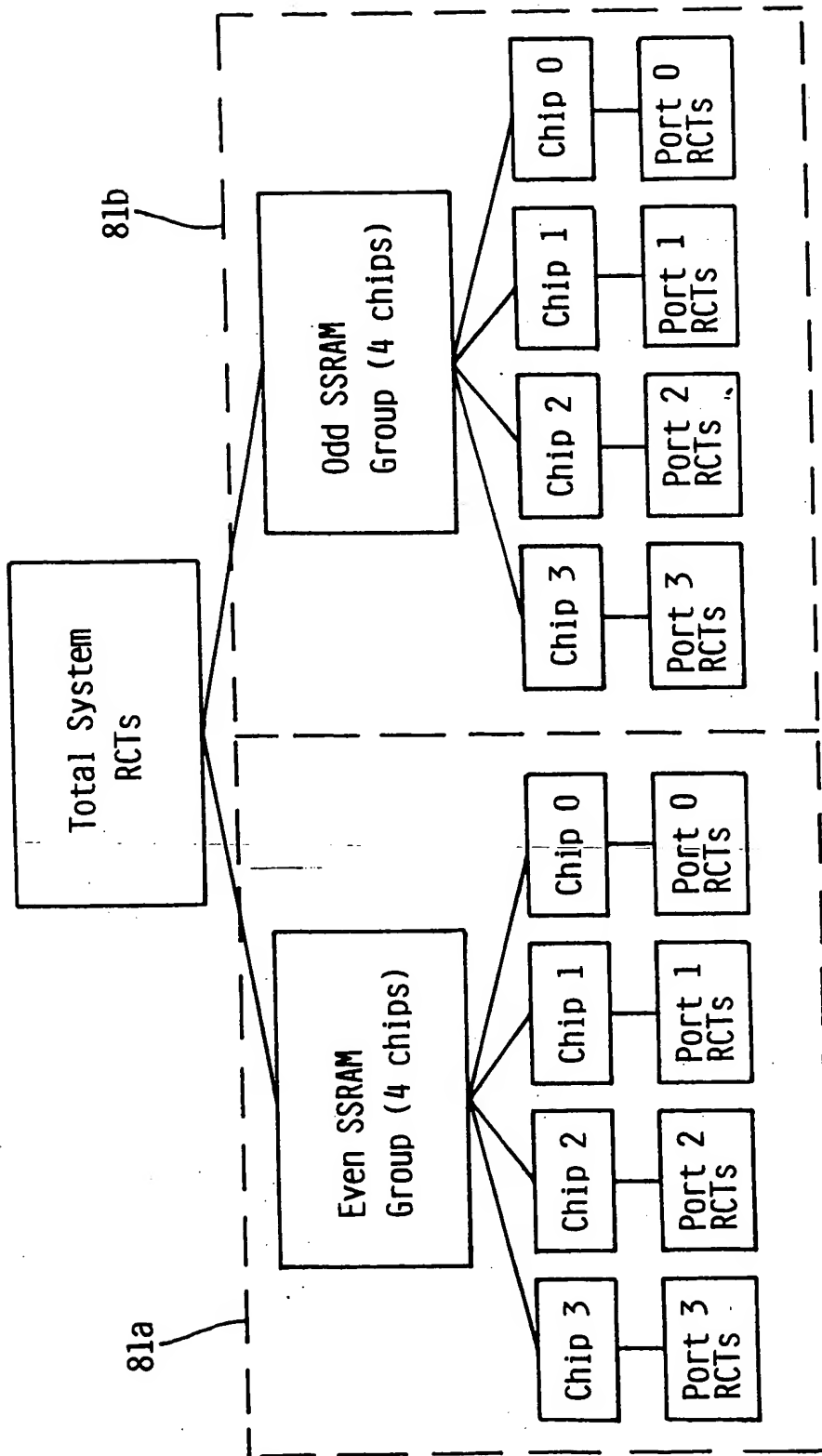


FIG. 12

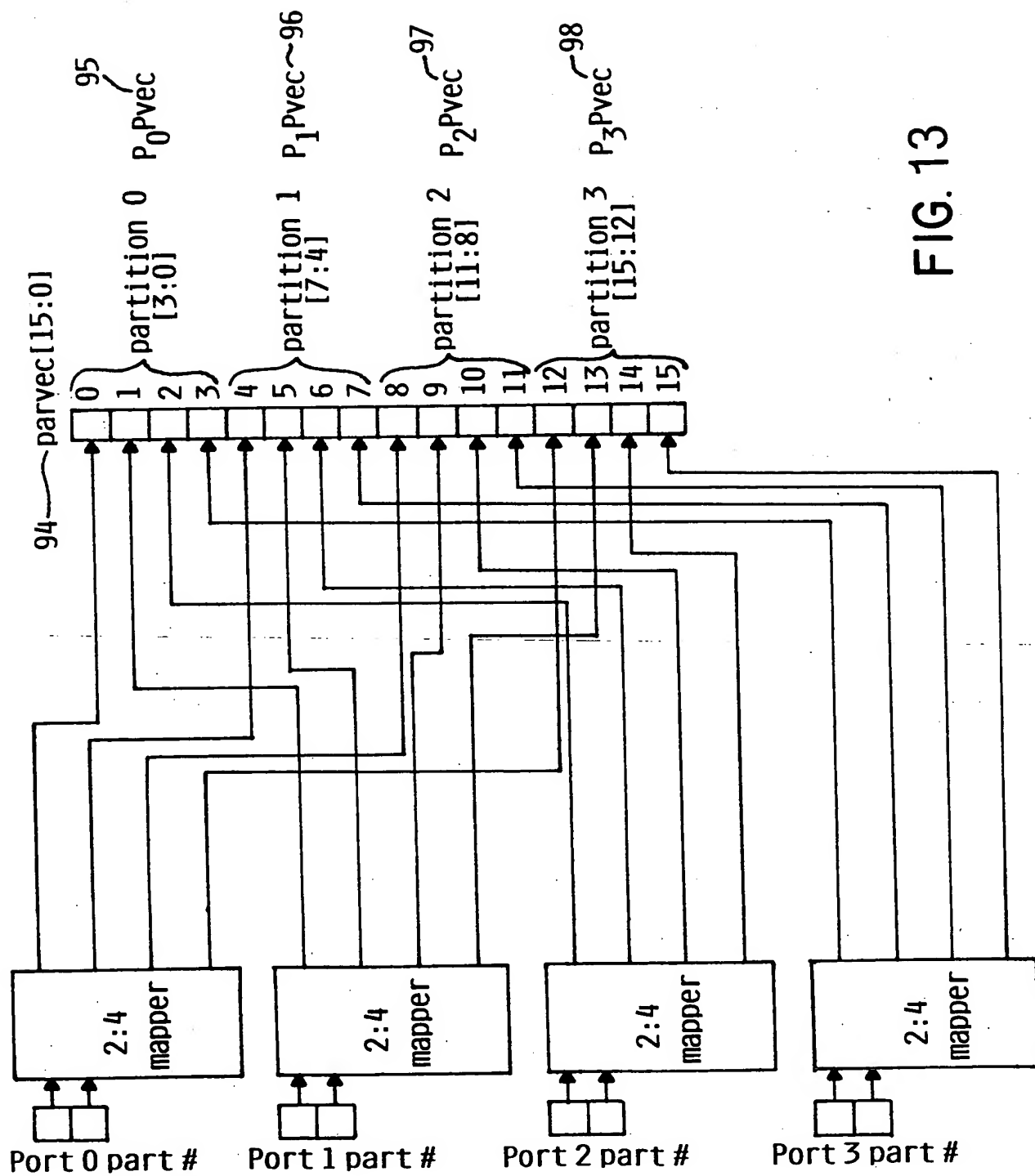


FIG. 13

Port 0 Partition  
Configuration Register

89

|    |              |    |
|----|--------------|----|
| 31 | Reserved     |    |
| 30 |              |    |
| 29 |              |    |
| 28 |              |    |
| 27 |              |    |
| 26 |              |    |
| 25 |              |    |
| 24 |              |    |
| 23 |              |    |
| 22 |              |    |
| 21 |              |    |
| 20 |              |    |
| 19 |              |    |
| 18 |              |    |
| 17 |              |    |
| 16 |              |    |
| 15 |              |    |
| 14 |              |    |
| 13 |              |    |
| 12 |              |    |
| 11 |              |    |
| 10 |              |    |
| 9  |              |    |
| 8  | P0Enable     | 91 |
| 7  |              |    |
| 6  | P0PartWrProt | 77 |
| 5  | P0PortWrProt | 78 |
| 4  | P0Commit     | 79 |
| 3  | P0LogID      | 92 |
| 2  |              |    |
| 1  | P0ParNum     | 93 |
| 0  |              |    |

FIG. 14

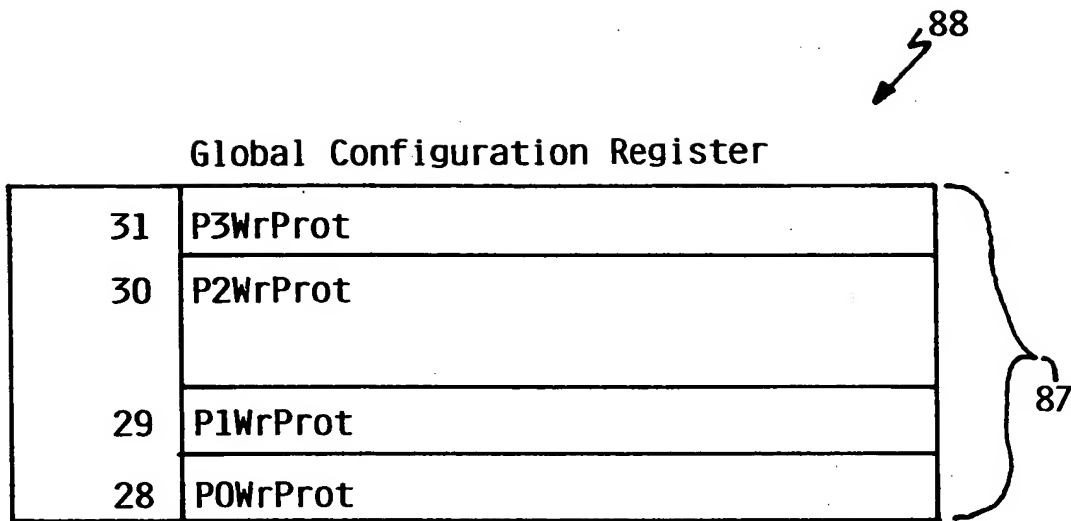


FIG. 15